Electronic Acknowledgement Receipt

EFS ID:	2416024
Application Number:	10678999
International Application Number:	
Confirmation Number:	8928
Title of Invention:	Adaptive clock-less equalizer circuit
First Named Inventor/Applicant Name:	Ramin Shirani
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Filer:	Kelvin M. Vivian/Kym Moore
Filer Authorized By:	Kelvin M. Vivian
Attorney Docket Number:	2644P
Receipt Date:	01-NOV-2007
Filing Date:	02-OCT-2003
Time Stamp:	20:08:26
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Notice of Appeal Filed	2644P_NoticeofAppeal.pdf	17352 5a80bb3ea508b414688028100ef006ae63c0cfbe	no	1
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8126 4c18569a3e204e6ca28604b86dc7563e79be3d9f	no	2
Warnings:					
Information:					
		Total Files Size (in bytes):	25478		

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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New International Application Filed with the USPTO as a Receiving Office
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